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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10004974	FILING DATE 03/01/2002	CLASS 327	SUBCLASS 157	GAU 2816	EXAMINER TON
**APPLICANTS: Aoki Koji; Sakima Hiroyuki;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-089880 03/27/2001					
PRE-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials MT		ATTORNEY DOCKET NO 108075-00079			
TITLE : Mode switching method for PLL circuit and mode control circuit for PLL circuit					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims Print Claim for O.G.	
ISSUE FEE Amount Due Date Paid		DRAWING Sheets Drwg. Figs.Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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